Improving Postgres' Concurrency

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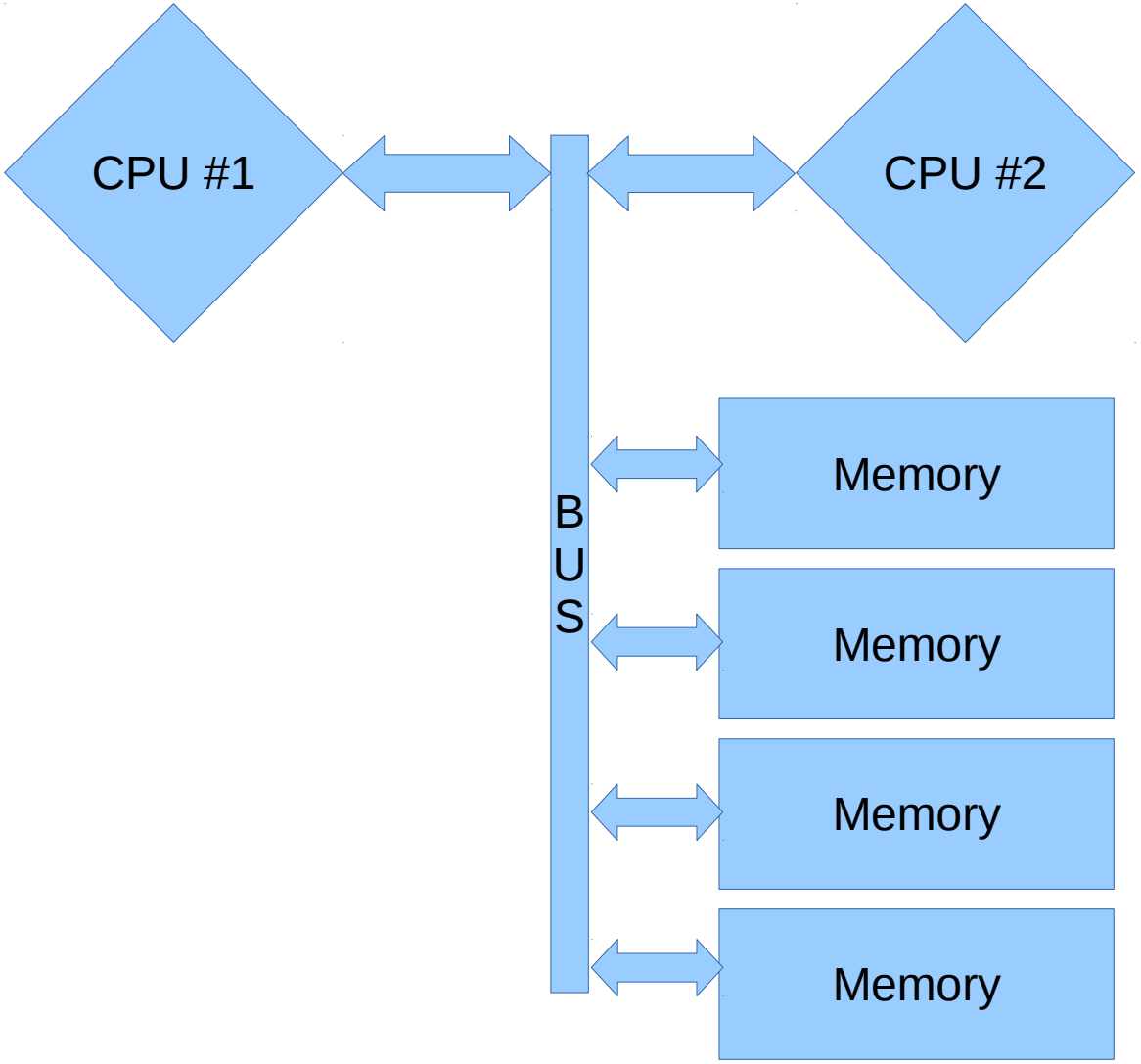
Vertical Scalability

- Bigger Machines → faster
- Multi-Core CPUs
  - 2005 - 2 cores
  - 2015 – 18 cores
- Multi-Socket Servers
- NUMA
- Cache Coherency
- Often cheaper to develop for
- Lower Latency / Higher Consistency

Uniform Memory Access



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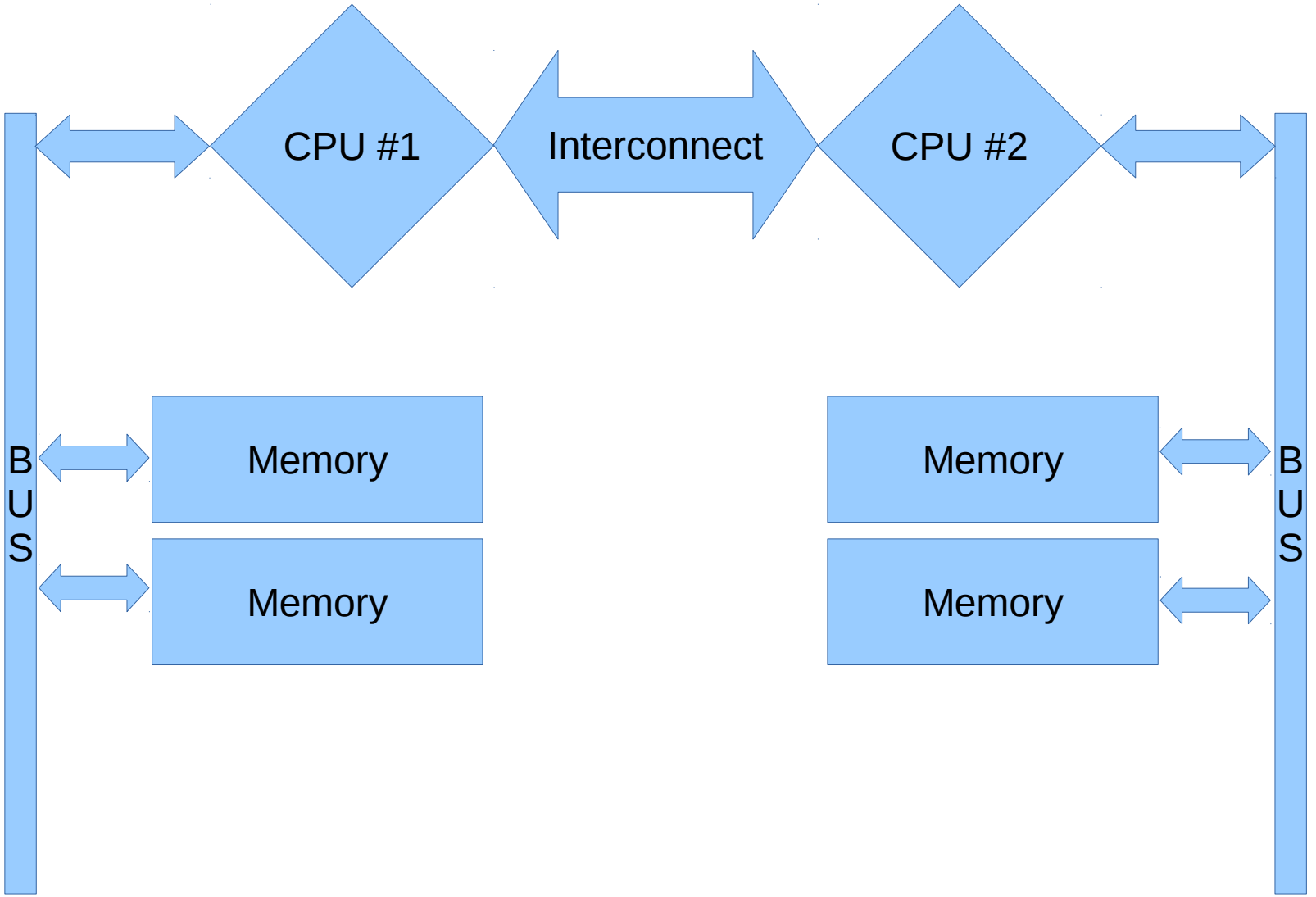
Non-Uniform Memory Access



Postgres Locking Primer

- Spinlocks
  - fast (very short locks)
  - exclusive only
  - no queuing (super expensive if locks held too long)
  - no error recovery
  - no deadlock checks
  - fixed number

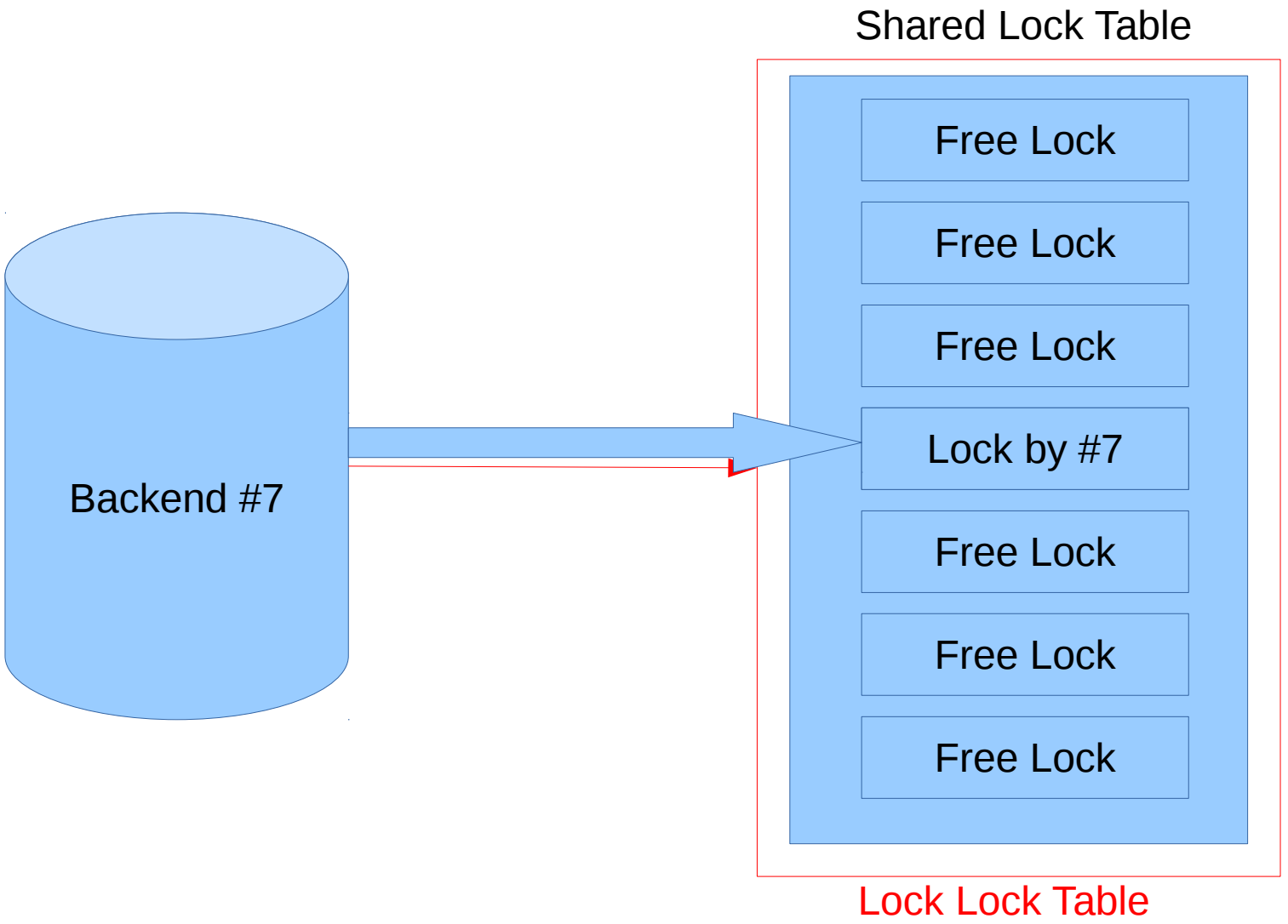
Postgres Locking Primer

- LWLock
  - fast
  - reader/writer lock
  - error recovery
  - no deadlock checks
  - fixed number
  - uses spinlocks

Postgres Locking Primer

- Heavyweight Locks
  - complex locking modes
  - error recovery
  - deadlock checks
  - “dynamic” identities
  - uses LWLocks & spinlocks

Acquiring a Heavyweight Lock



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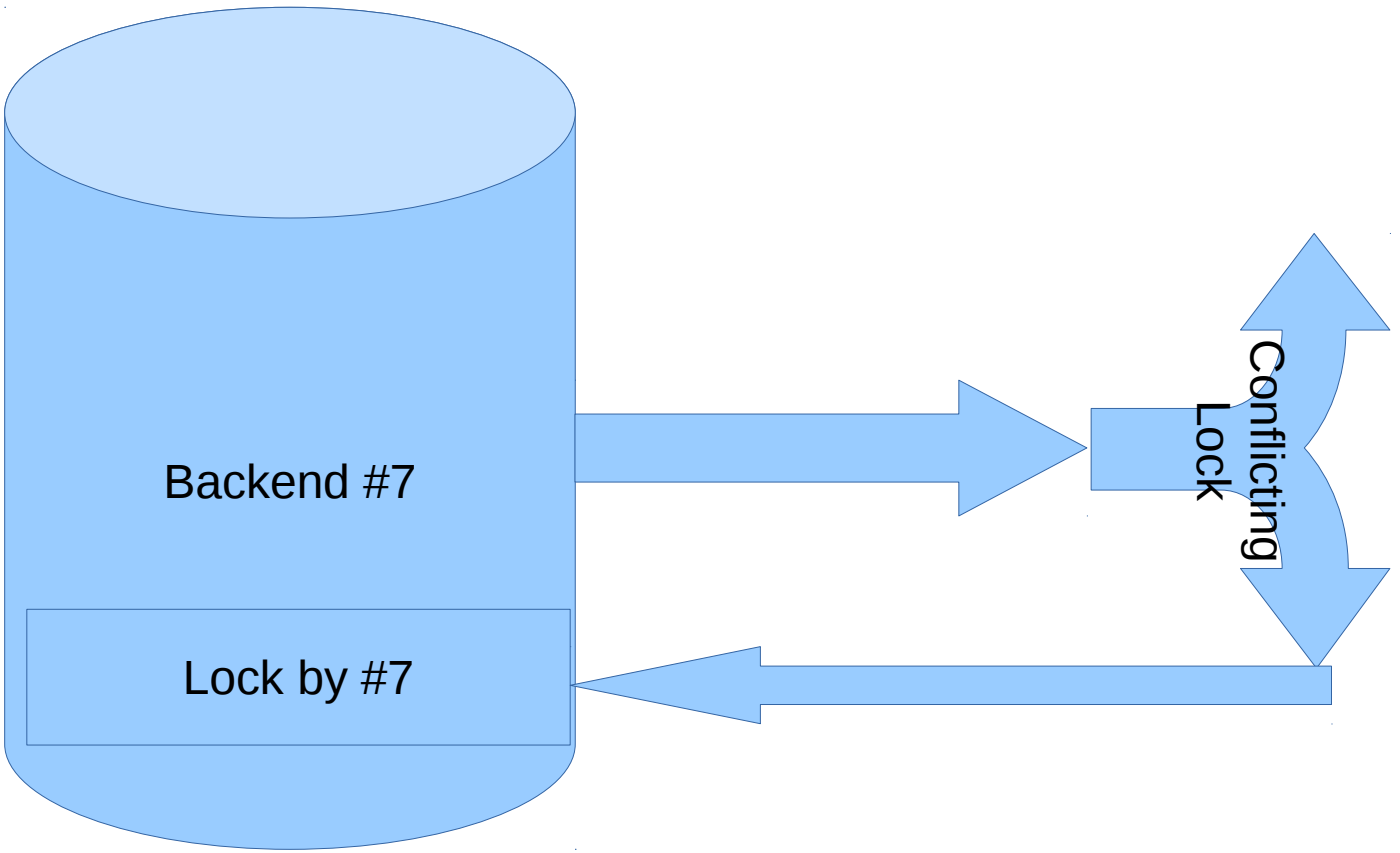
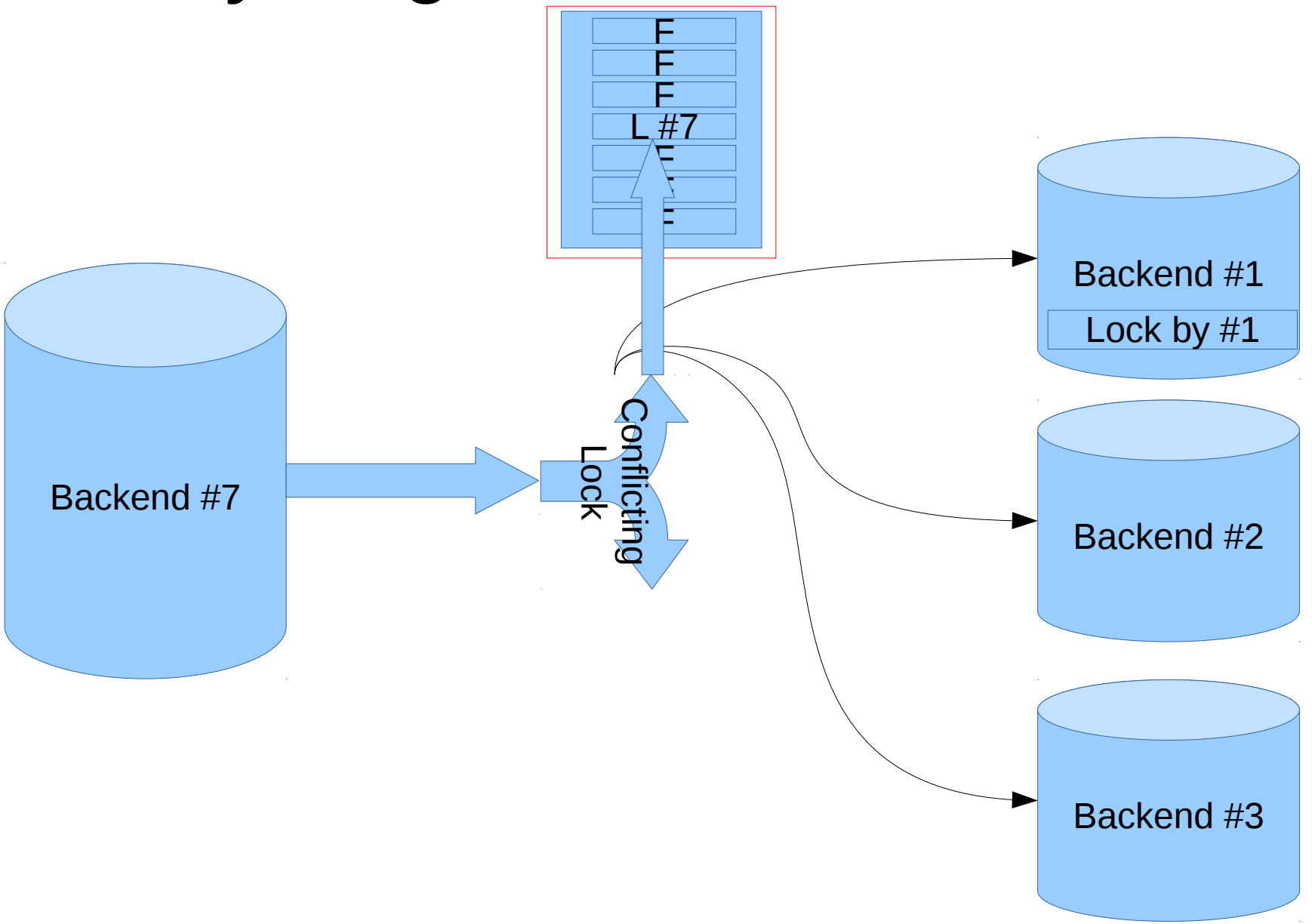
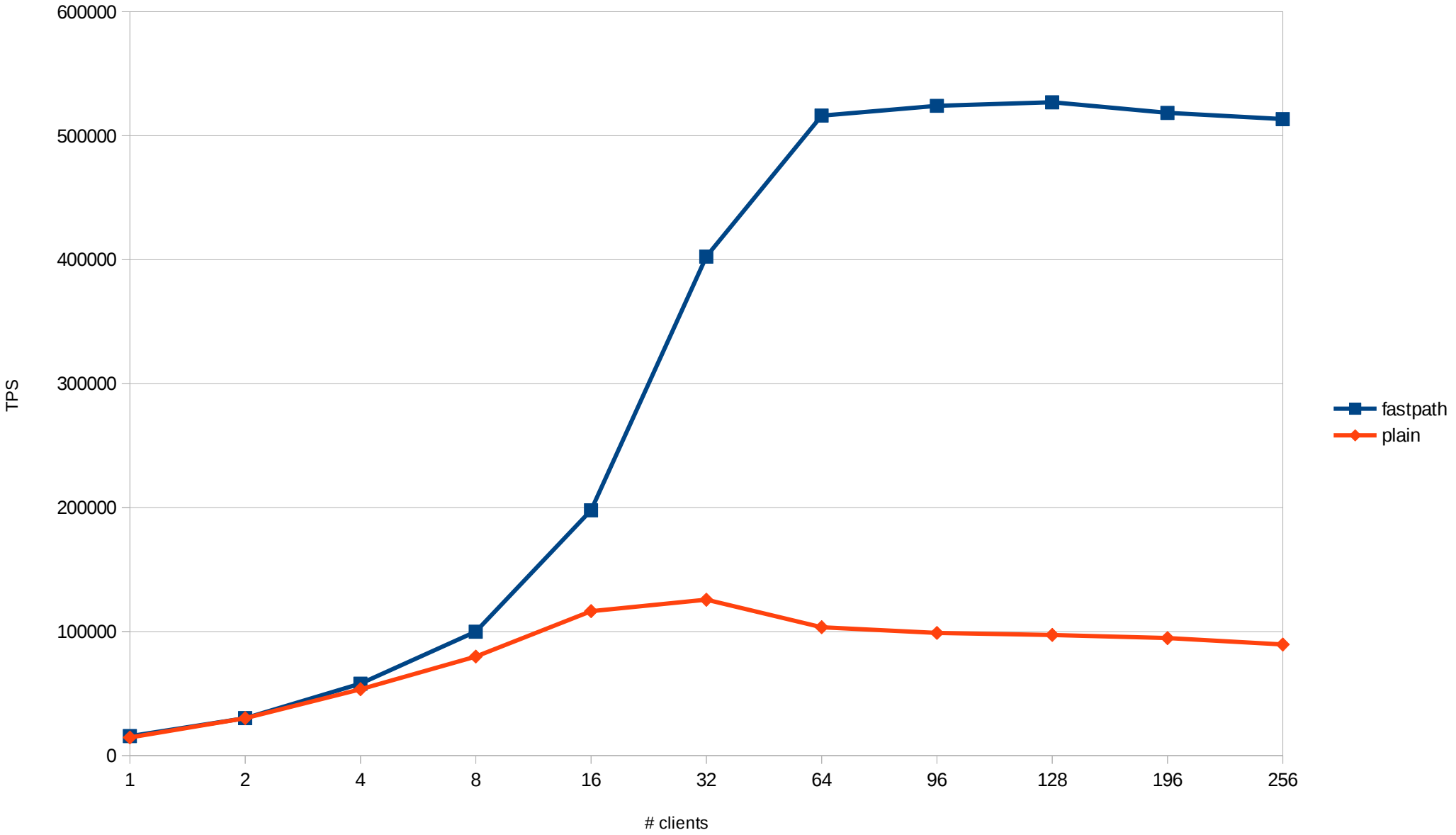
Heavyweight Lock - Fastpath



Heavyweight Lock – Slow Path





- readonly pgbench scale 300
- EC2 m4.8xlarge - 2 x E5-2676
- master @ aa6b2e6
- fastpath disabled in code

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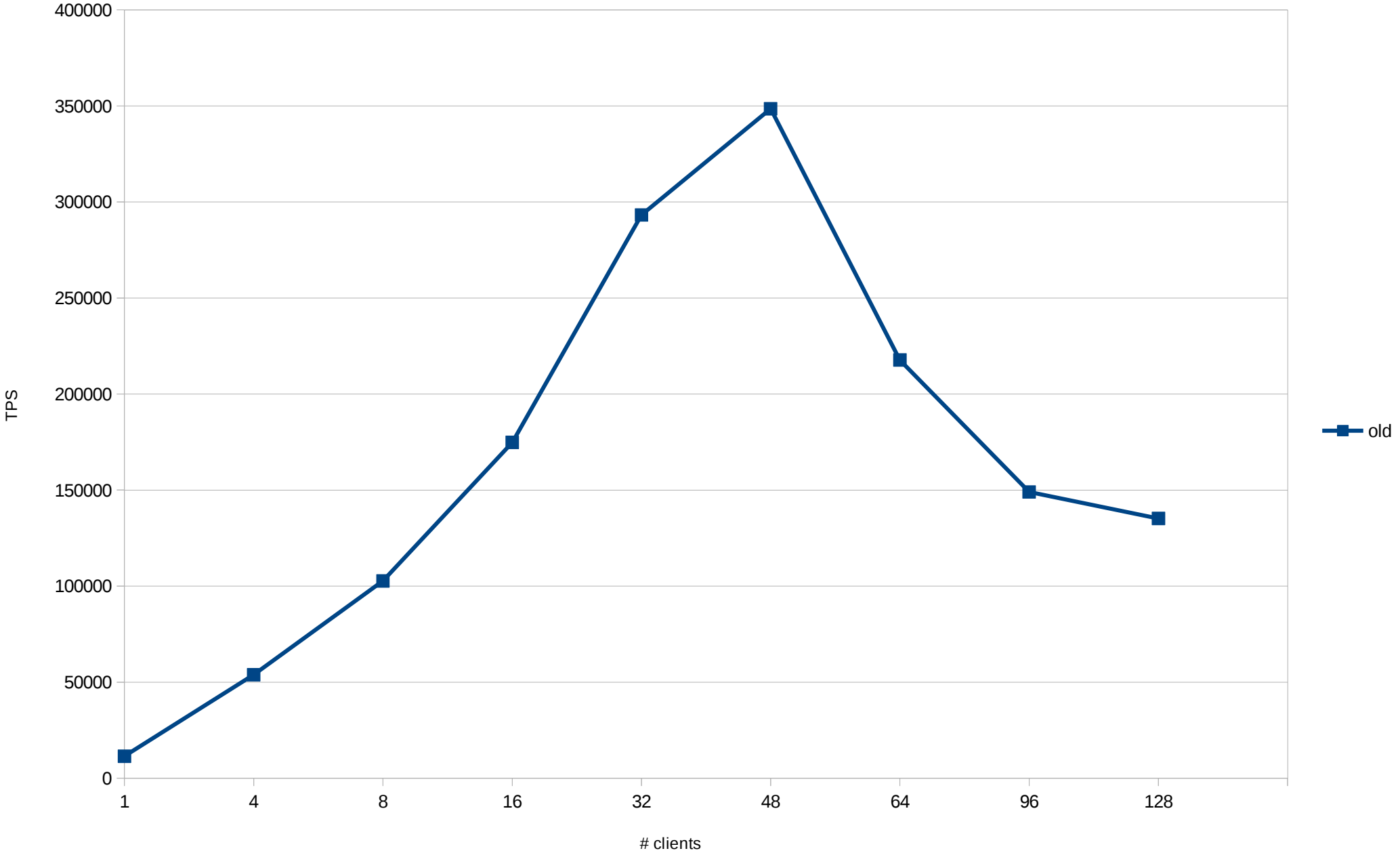
LWLock scalability

```
# perf top -az
     89.53%  postgres  postgres  [.] s_lock
      2.53%  postgres  postgres  [.] LWLockAcquire
      1.79%  postgres  postgres  [.] LWLockRelease
      0.63%  postgres  postgres  [.] hash_search_..._value
```

```
LWLockAcquire(LWLock *l, LWLockMode mode)
{
  retry:
    SpinLockAcquire(&lock->mutex);

    if (mode == LW_SHARED)
    {
        if (lock->exclusive)
        {
            lock->shared++;
        }
        else
        {
            QueueSelf(l);
            SpinLockRelease(&lock->mutex);
            WaitForRelease(l);
            goto retry;
        }
    }
    ...
    SpinLockRelease(&lock->mutex);
}
```



- readonly pgbench
- 4xE5-4620
- scale 100

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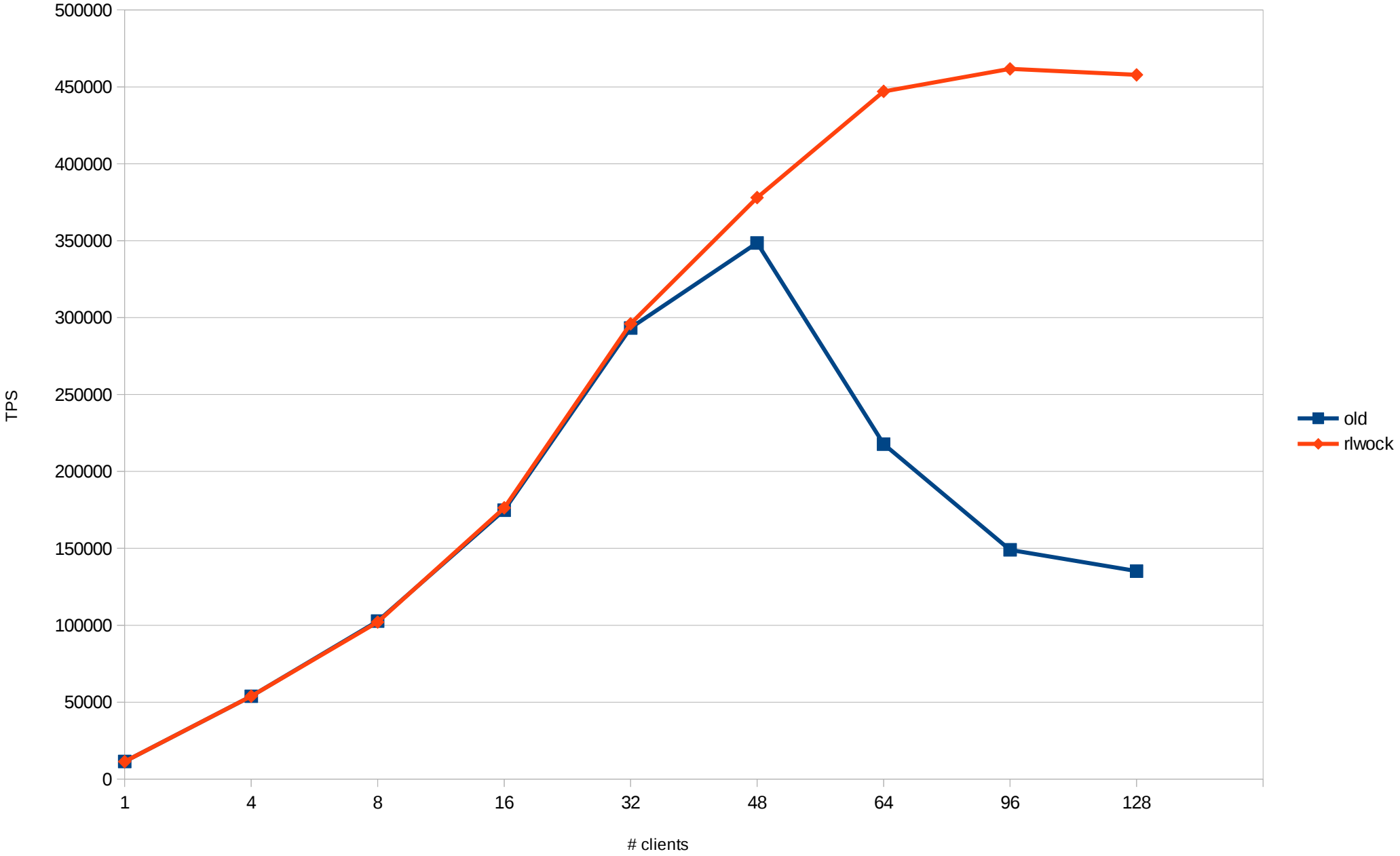
Fix this!

- Use atomic operations
  - atomic add & subtract, compare exchange
- Complex, due to queuing

```
if (atomic_lock_acquire(lock, mode))
{
   QueueSelf();
   if (atomic_lock_acquire(lock, mode))
      UnQueueSelf();
   else
      WaitForRelease();
      goto retry;
}
```



- readonly pgbench
- 4xE5-4620
- scale 100

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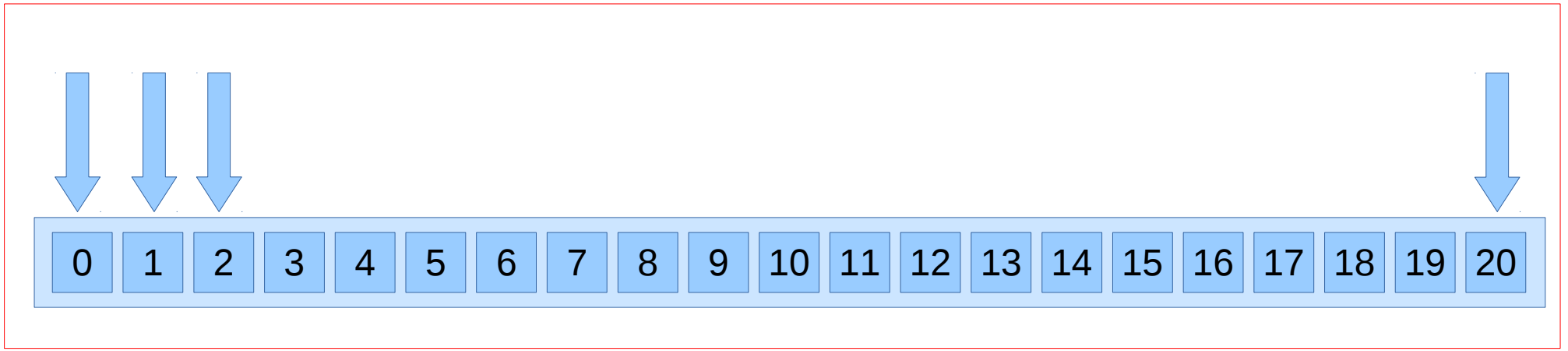
Buffer Descriptors & Buffers

```
struct BufferDesc
{
    BufferTag   tag;          /* ID of page contained in buffer */
    BufFlags    flags;        /* see bit definitions above */
    uint16      usage_count;  /* usage counter for clock sweep */
    unsigned refcount;        /* # of backends holding pins */

    slock_t     buf_hdr_lock; /* protects the above fields */
} BufferDesc;
```

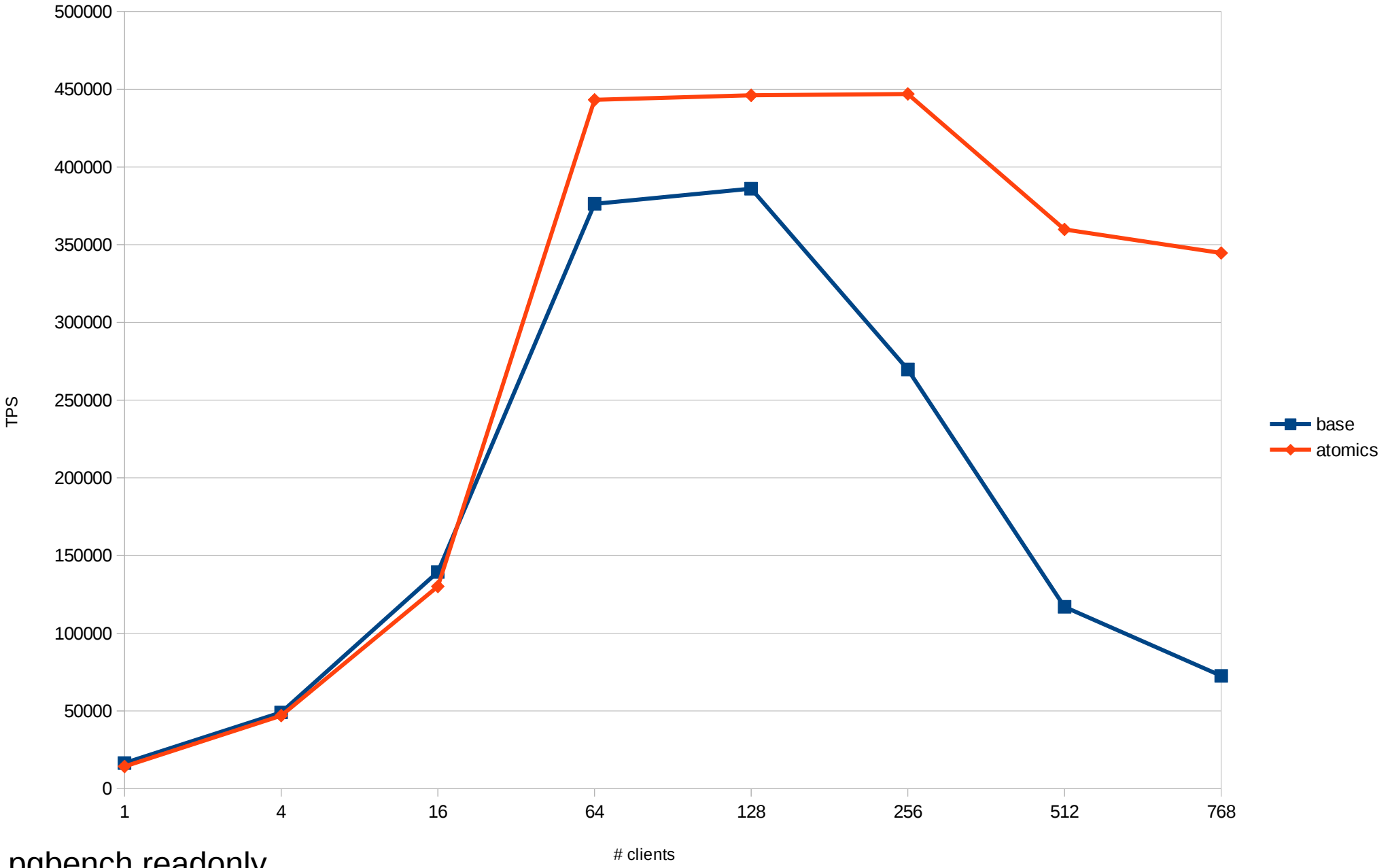
Inefficient Buffer Replacement



Fix It

- granular locking: spinlock per clock tick
- atomics:

```
victim = pg_atomic_fetch_add_u32(
   &StrategyControl->nextVictimBuffer, 1);
```



- pgbench readonly
- scale 1000 (~14GB)
- 4GB shared buffers
- EC2 m4.8xlarge - 2 x E5-2676
- master @ aa6b2e6

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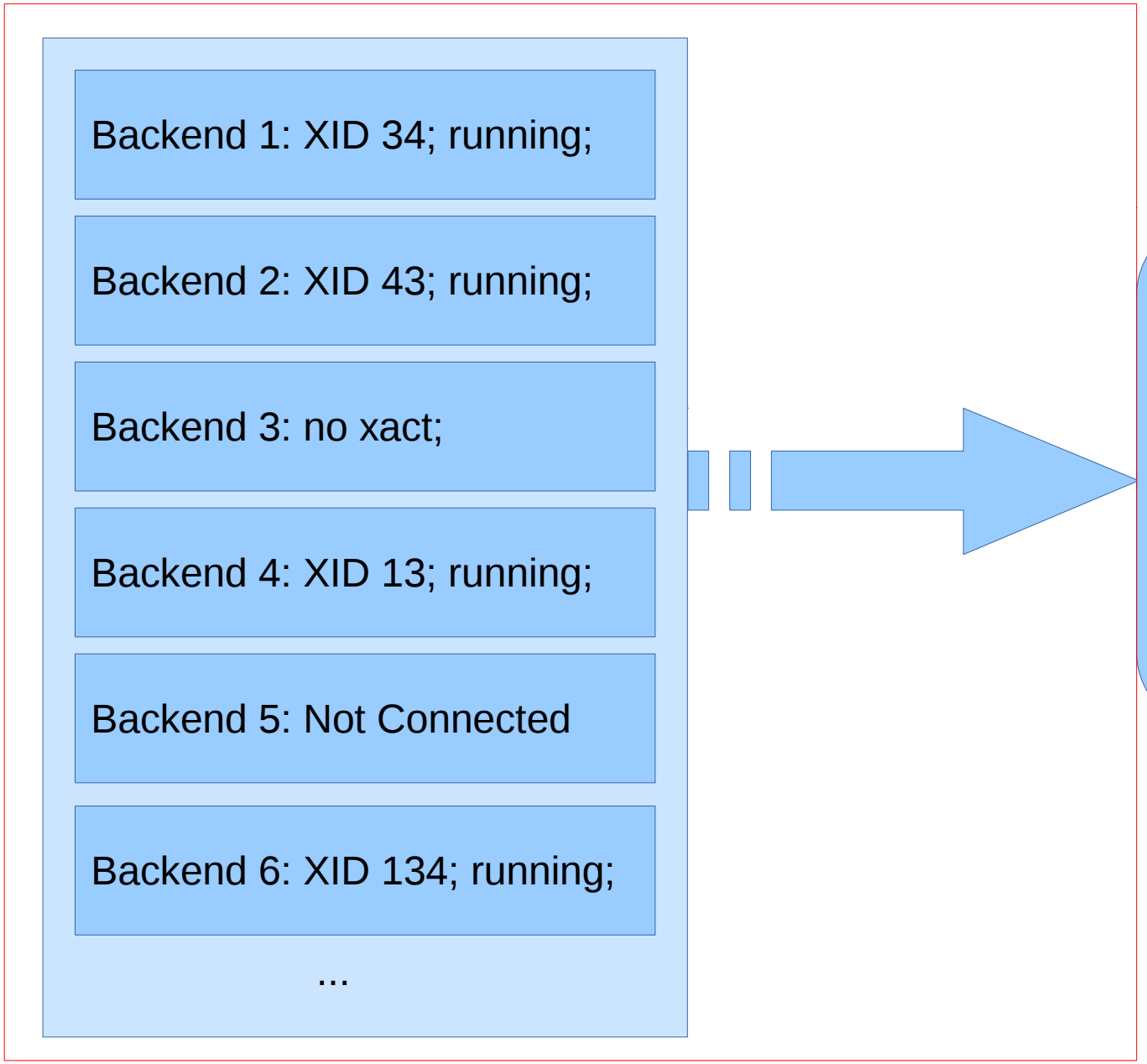
Scalability Approaches

- Avoid locks in common cases
- More efficient locking
- Atomic operations
- More granular locking

Not Yet Fixed Scalability Issues

- Extension Lock
  - Problematic: Bulk write workloads
- Buffer Replacement Complexity & Accuracy
  - Problematic: Larger than memory workloads
- Expensive Snapshot Computation
  - Problematic: High QPS (combined read & write) workloads
- Buffer Pins use spinlocks
  - Problematic: Lots of accesses to the same buffer

Expensive Snapshot Computation



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